

Text fixture

Example 1

```
initial
begin
a=2'b10;
b=2'b00;
```

```
#100;
```

```
a=2'b11;
b=2'b10;
```

```
#100;
```

```
a=2'b11;
b=2'b10;
```

```
#100;
```

```
a=2'b11;
b=2'b10;
```

```
#100;
```

```
a=2'b11;
b=2'b10;
```

```
#100;
```

Example 2

```
module prob1_tb();
  reg a,b,c,d;
  wire out;

  prob1 prob1_test(a,b,c,d, out);

  initial begin
    $monitor(a,b,c,d,out);
    for (int i=0; i<16; i=i+1) begin
      {a,b,c,d} = i;
      #1;
    end
  end
endmodule
```

/*

Output:

00000
00010
00100
00110
01000
01010
01100
01110
10000
10010
10100
10110
11000
11010
11101
11110

*/